

10

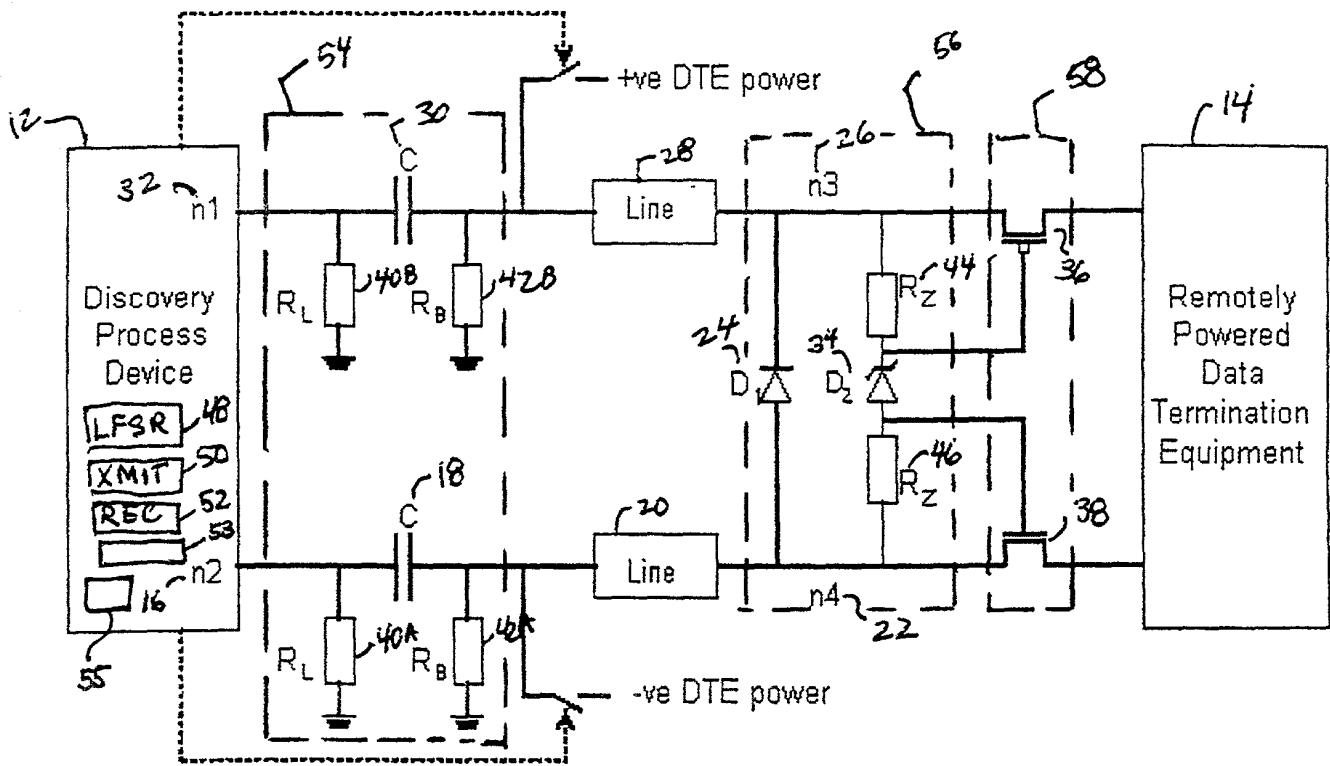
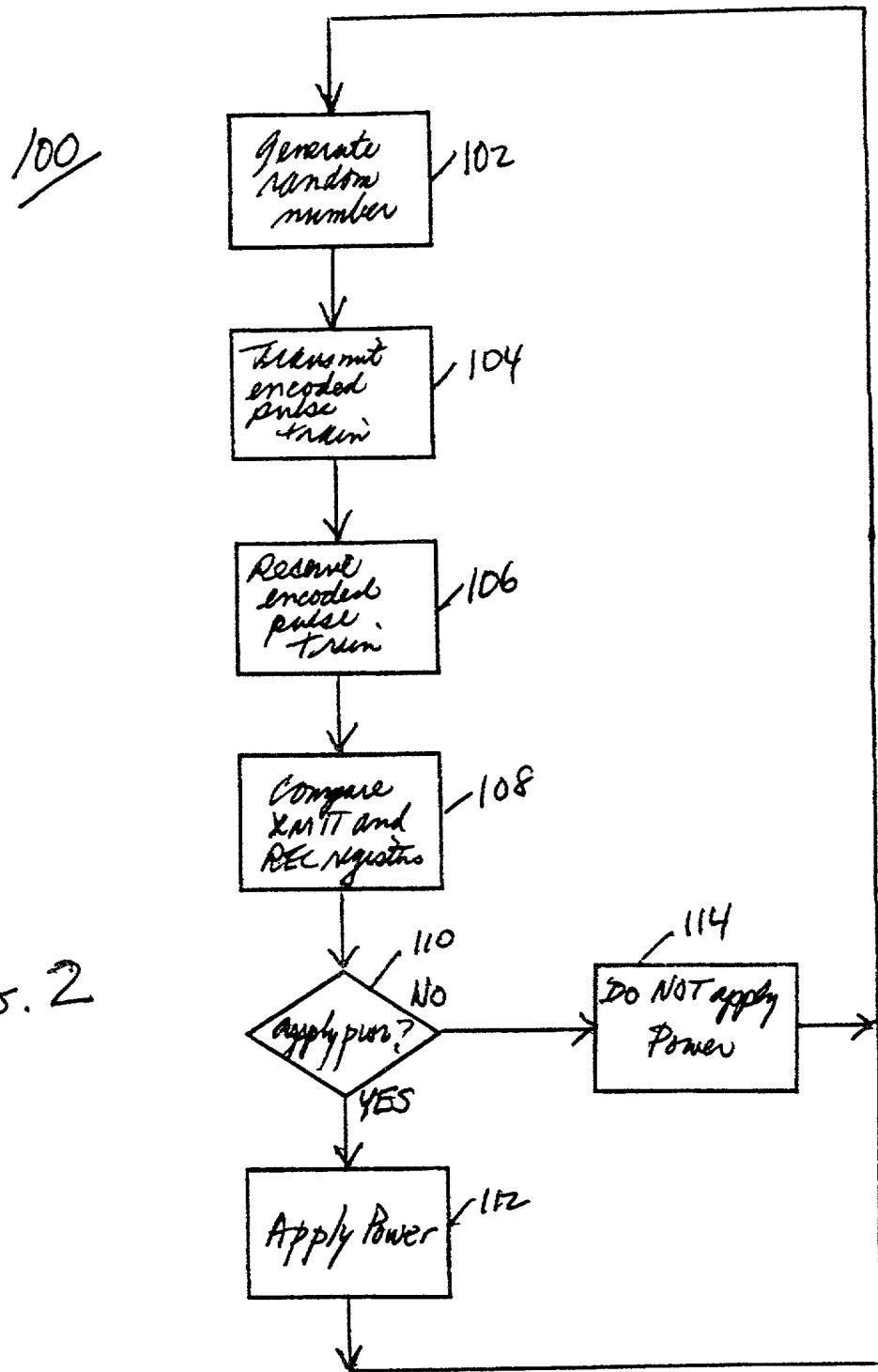


FIG. 1

FIG. 2



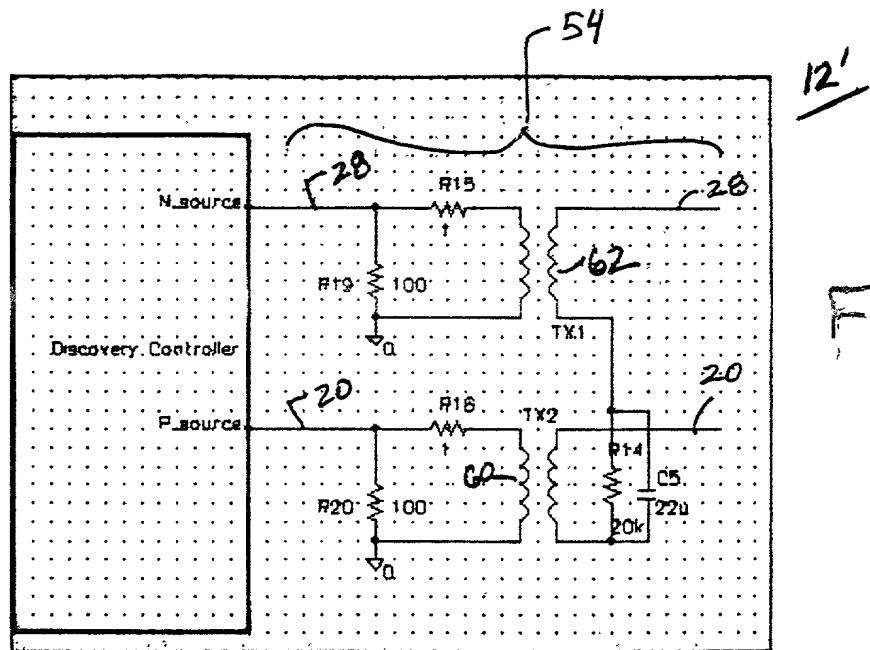


FIG. 3

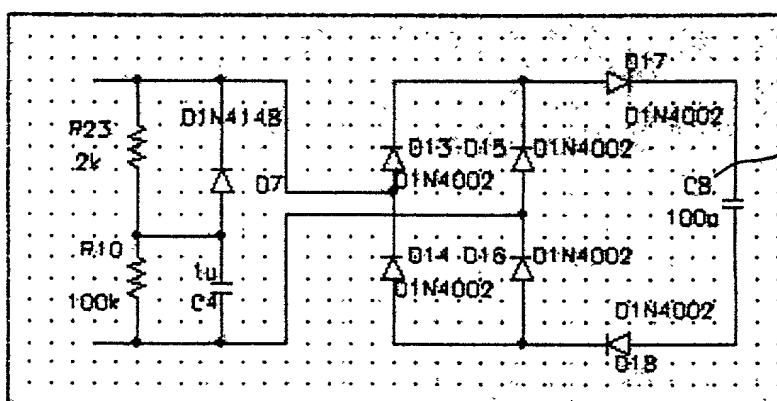


FIG. 4

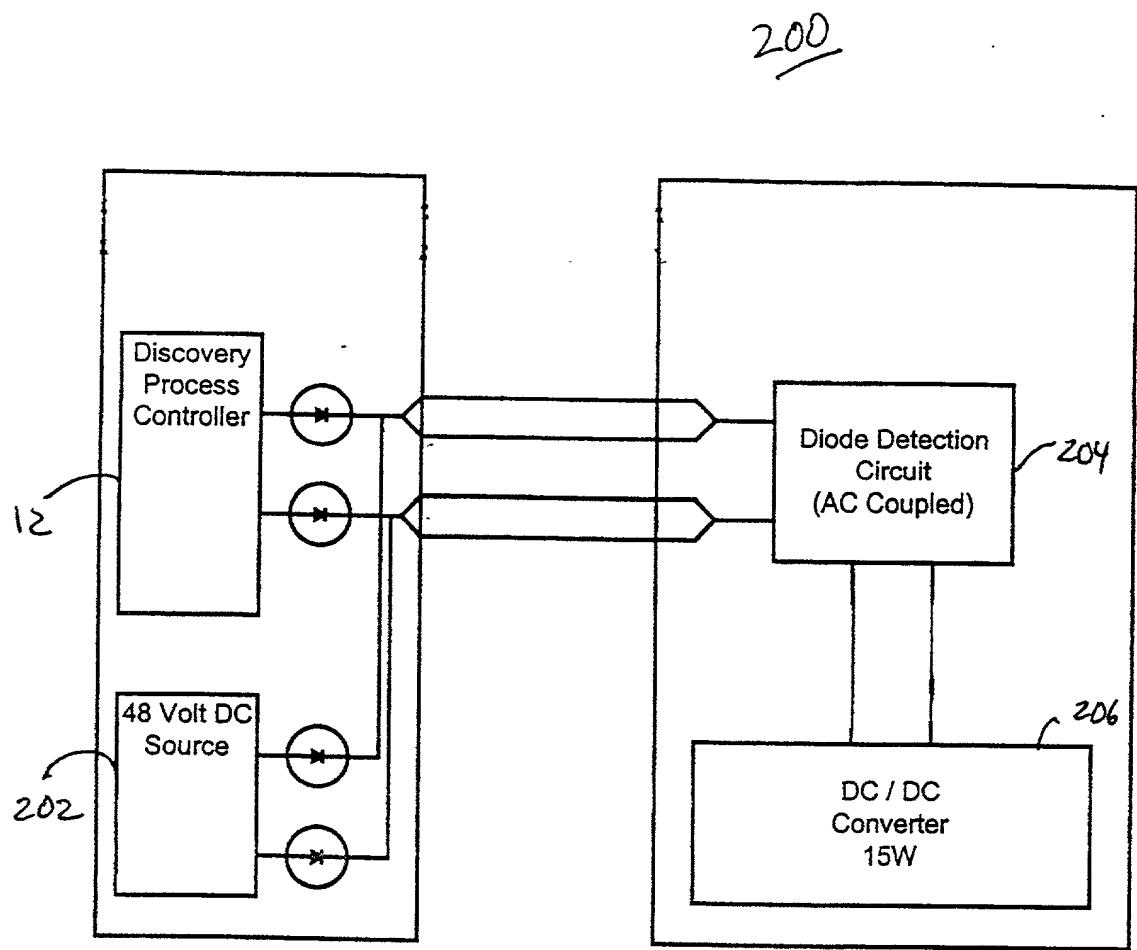
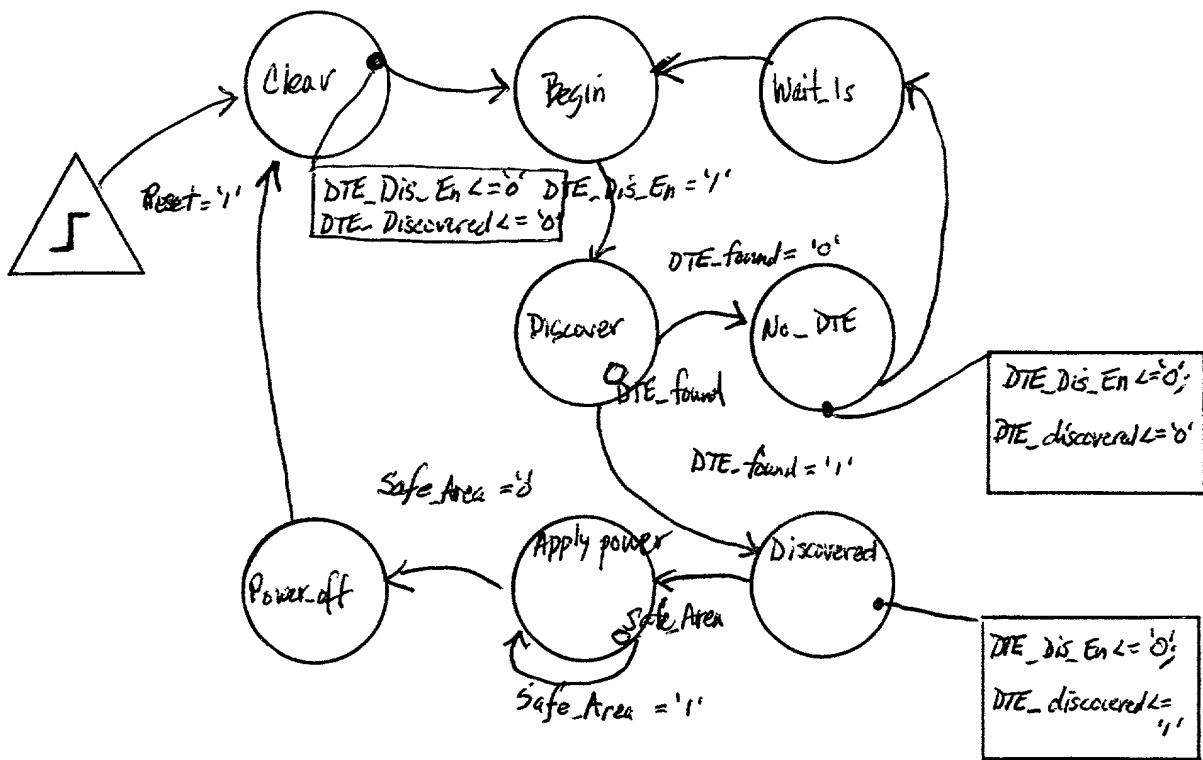


FIG. 5

250

$DTE_Dis_En = '0'$



F | G. 6

Power Level Detection Process

Primary Process Stage

Generate
Random
Number

306

Transmit
encoded pulse
train

308

Receive
encoded pulse
train

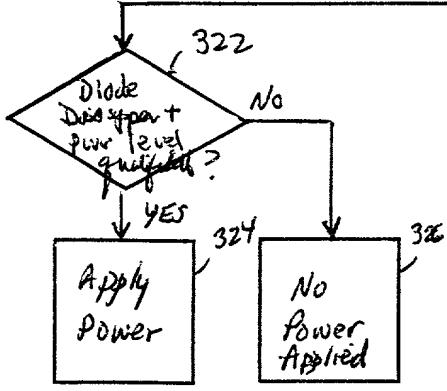
310

Compare XMIT
and REC
Registers

312

Determine
whether to
apply power

314



302

304

Secondary Process Stage

Reduce time
between
pulses

316

Measure time
for capacitors
to charge

318

Determine
power level
from time

320

FIG. 7